

# Automotive Systems Optimization Based on Safety Requirements

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## Abstract

An approach for system design and architecture optimization driven by safety and cost constraints is proposed in this paper. It consists of an architecture synthesis and mapping approach that takes into account the safety constraints in the ISO 26262 context. This approach makes possible to reach a system preliminary architecture by choosing the best component that reduce the overall cost. On the other hand, it leads to a mapping that respects the safety constraints related to safety levels and to dependent failures. Exhaustive and genetic algorithm based approaches are used for the optimization. The use of these two approaches depends on the size of the considered problem. The authors demonstrate that these approaches can be used efficiently to reach an optimal design.

**Keywords:** Safety, Functional Safety, Architecture Optimization, Automotive, Genetic algorithm

## References:

- [1] ISO 26262: Road Vehicles – Functional safety, International Organization for Standardization (2011)
- [2] H. Nikolov and M. Thompson, “Daedalus: toward composable multimedia MP-SoC design”. Proceedings of the 45th annual Design Automation Conference. ACM, 2008. p. 574-579.
- [3] J. Keinert, M. Streubühr, T. Schlichter, J. Falk, J. Gladigau, C. Haubelt, J. Teich, and M. Meredith, “SystemCoDesigner – an automatic ESL synthesis approach by design space exploration and behavioral synthesis for streaming applications”, ACM Trans. Des. Autom. Electron. Syst., vol. 14, no. 1, pp. 1-23, Jan. 2009.
- [4] R. Dömer, A. Gerstlauer, J. Peng, D. Shin, L. Cai, H. Yu, S. Abdi, and D. Gajski, “System-on-Chip Environment: A SpecC-Based Framework for Heterogeneous MPSoC Design”, EURASIP J. Embed. Syst., vol. 2008, no. 1, p. 647953, 2008.
- [5] A. Gerstlauer and C. Haubelt, “Electronic system-level synthesis methodologies”. Computer-Aided Design of Integrated Circuits and Systems, IEEE Transactions on, 2009, vol. 28, no 10, p. 1517-1530.
- [6] A. Aleti and S. Bjornander, “ArcheOpterix: An extendable tool for architecture optimization of AADL models”, Model-Based Methodologies for Pervasive and Embedded Software, 2009. MOMPES'09. ICSE Workshop on. IEEE, 2009. p. 61-71.
- [7] Y. Papadopoulos, M. Walker, D. Parker, E. Rude, R. Hamann, A. Uhlig, U. Grätz, and R. Lien, “Engineering failure analysis and design optimisation with HiP-HOPS”, Eng. Fail. Anal., vol. 18, no. 2, pp. 590–608, Mar. 2011.
- [8] M. Dhouibi, J. Perquis, L. Saintis, and M. Barreau, “An Optimization Approach for Automotive Systems Architecture Driven by Safety and Cost”, Accepted Paper, Lambda Mu 19, 2014.

- [9] M. Dhouibi, J. Perquis, L. Saintis, and M. Barreau, "Automatic Decomposition and Allocation of Safety Integrity Level Using System of Linear Equations", ... Complex Syst. ..., no. c, pp. 1-5, 2014.
- [10] K. Meffert, N. Rotstan, C. Knowles, and U. Sangiorgi, "Jgap-java genetic algorithms and genetic programming package", URL <http://jgap.sf.net>, 2012.